

Figure 1: Composite Device

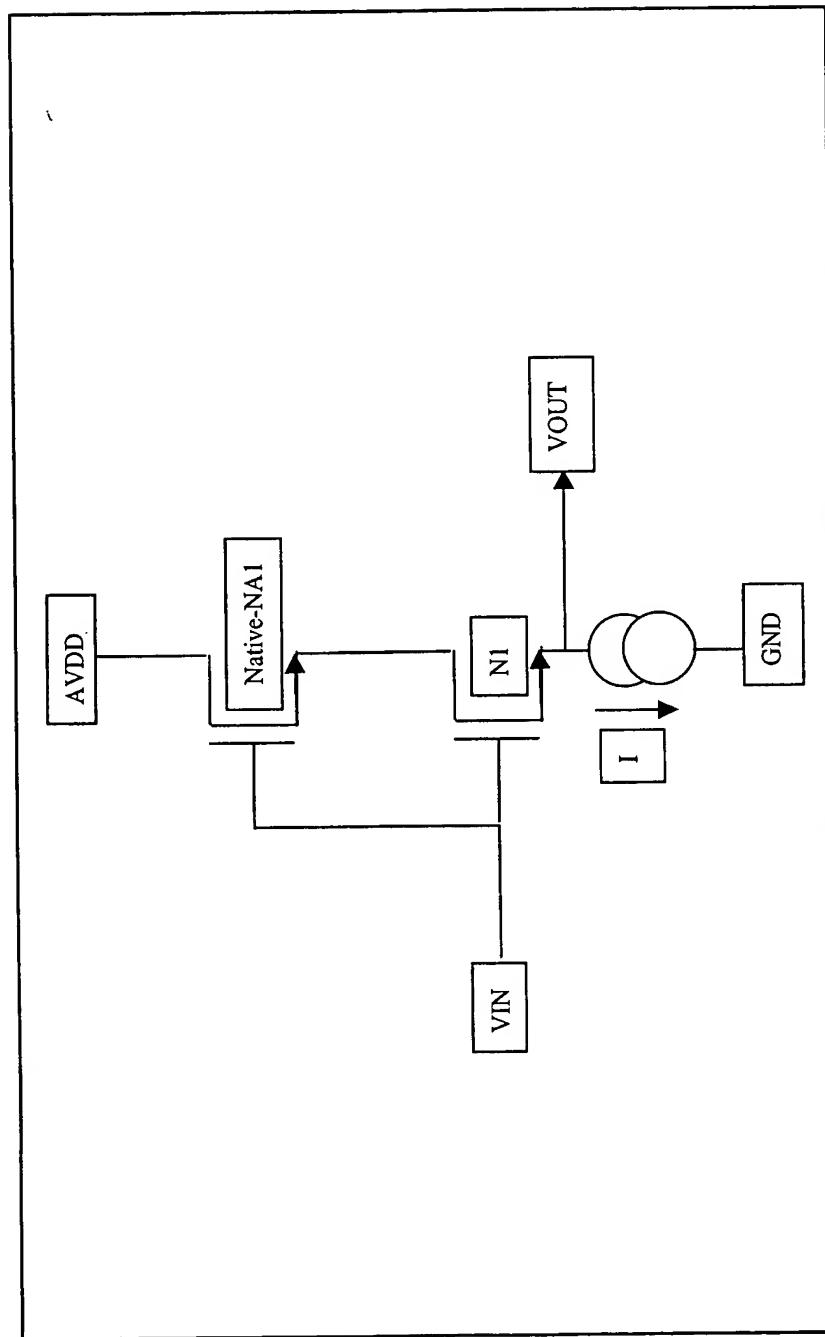


Figure 2: Source-follower Configuration

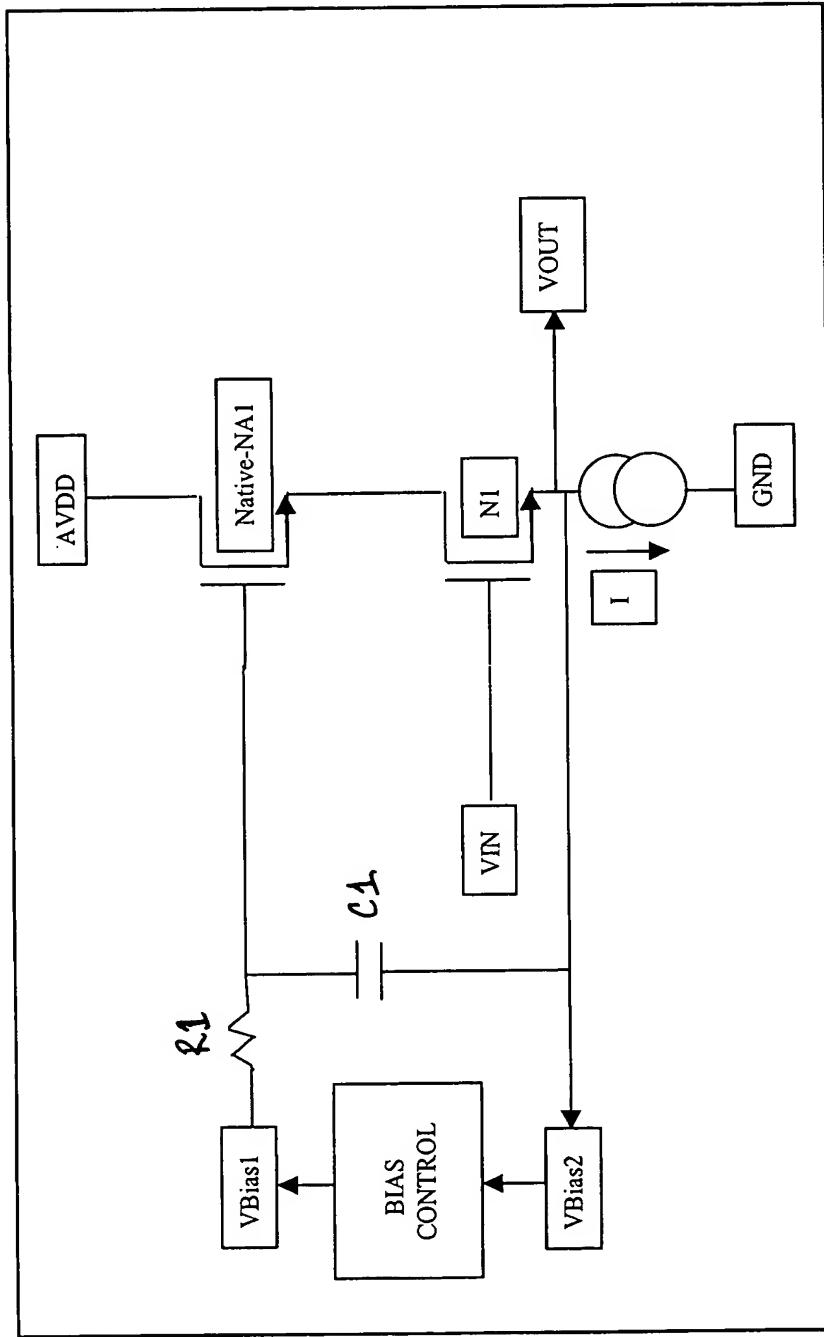


Figure 3: AC-coupled Bias of Native Gate

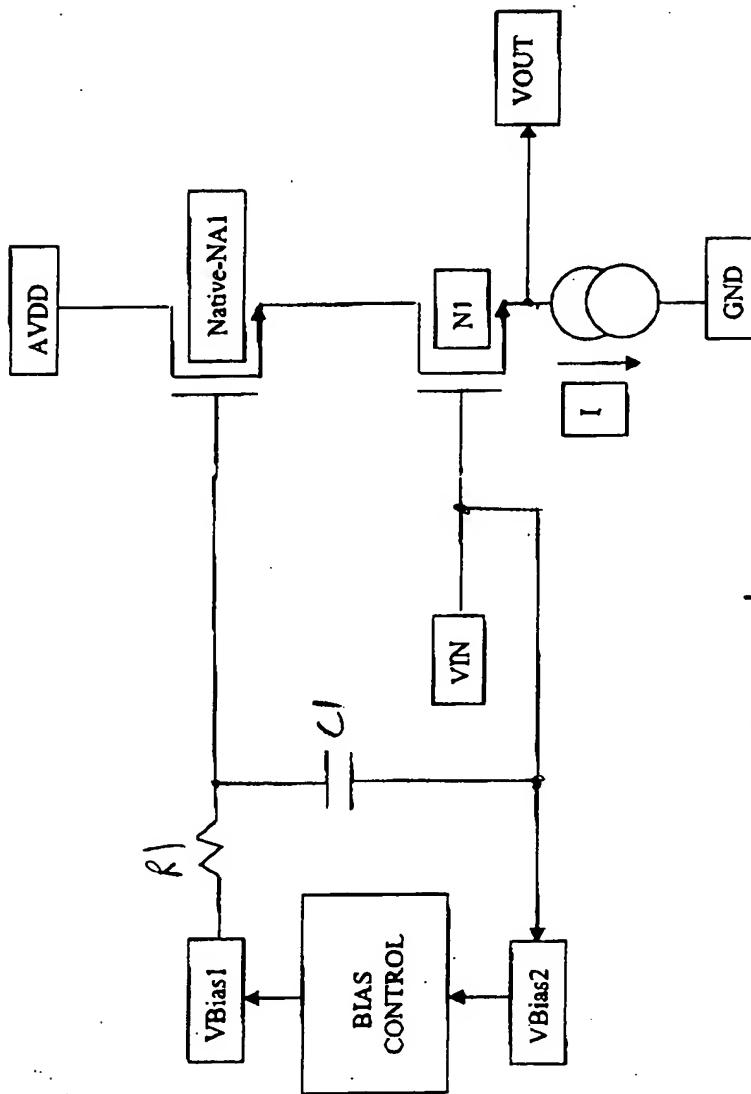


Figure 4

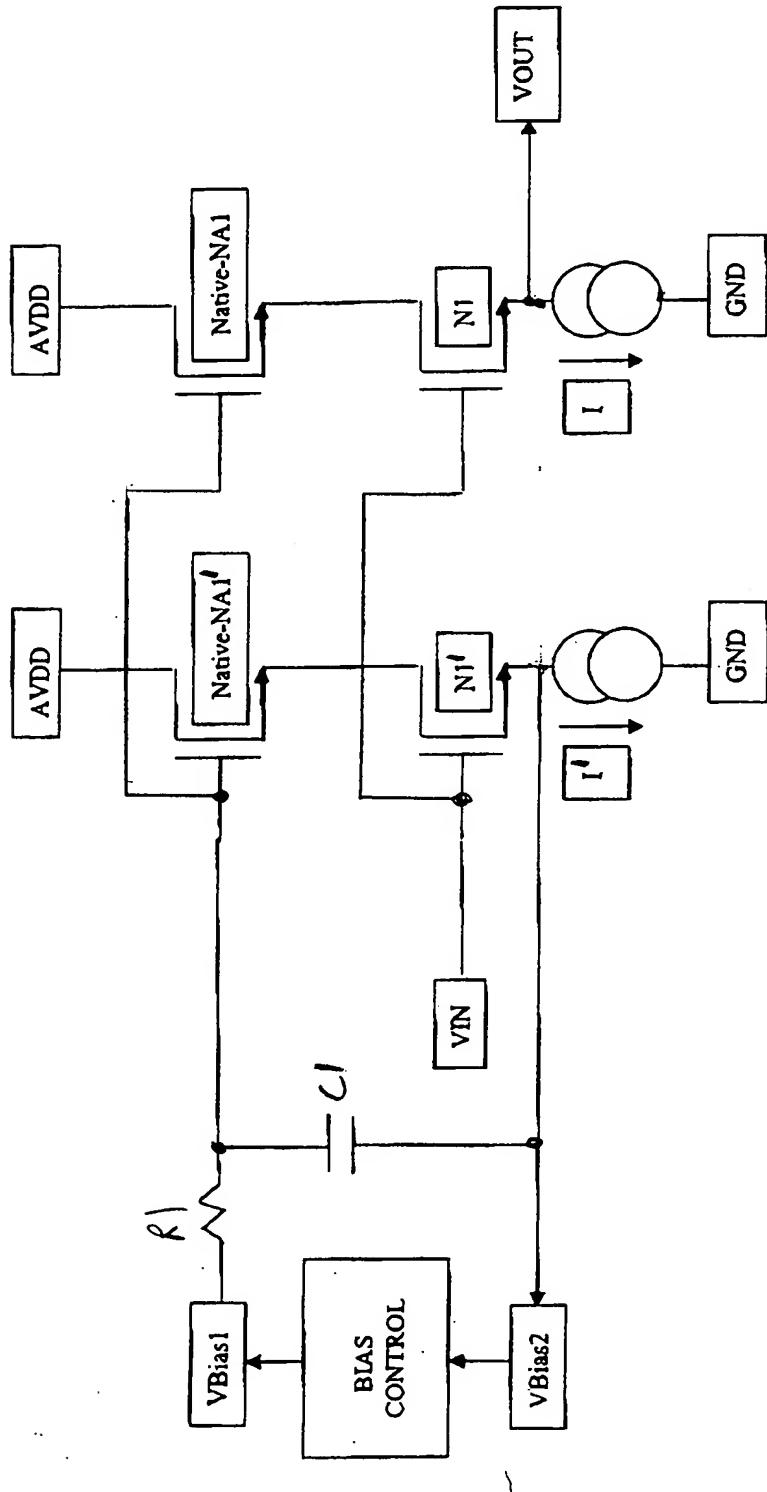


Figure 5